

## **Embedding Artificial Intelligence on Low Complexity and Low Power Processors**



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### **Abstract**

Artificial Intelligence (AI) and its usage is becoming quite regular and spread in our daily life. While applications such as chatbots, recommender systems, or spam filters rely on massive network structures that are deployed on remote servers, when it comes to natural language processing or healthcare monitoring, it is pretty crucial that AI runs its algorithms on a smartphone or a wearable device, both with memory and power restrictions. Also, running AI algorithms on the edge or on the device can solve many issues due to privacy concerns or bandwidth limitations. This keynote will discuss the challenges involved in performing the inference of AI networks on low-power devices with restrictions on the memory footprint available.

### **Short Bio:**

Sérgio Correia received his Diploma in Electrical and Computer Engineering from the University of Coimbra, Portugal, in 2000, the master's degree in Industrial Control and Maintenance Systems from Beira Interior University, Covilhã, Portugal, in 2010, and the Ph.D. in Electrical and Computer Engineering from the University of Coimbra, Portugal, in 2020. Currently, he is an Associate Professor at the Polytechnic Institute of Portalegre, Portugal. He is a Researcher at COPELABS - Cognitive and People-centric Computing Research Center, Lusofona University of Humanities and Technologies, Lisbon, Portugal, and VALORIZA - Research Center for Endogenous Resource Valorization, Polytechnic Institute of Portalegre, Portalegre, Portugal, and has worked with several private companies for more than 20 years. His current research interests are Wireless Sensor

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